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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)



Complete if Known

Application Number	10/043065
Filing Date	January 9, 2002
First Named Inventor	Forbes, Leonard
Group Art Unit	2822
Examiner Name	Meier, Stephen

Sheet 1 of 1

Attorney Docket No: 303.684US3

**US PATENT DOCUMENTS**

Examiner Initial*	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date if Appropriate
KR	US-2001/0011744	08/09/2001	Sung, K.	257	314	03/30/2001
	US-2002/0020871	02/21/2002	Forbes, L.	257	315	08/30/2001
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**FOREIGN PATENT DOCUMENTS**

Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T <sup>2</sup>
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**OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>3</sup>
KR		SHI, Y., et al., "Tunneling Leakage Current in Ultrathin (<4 nm) Nitride/Oxide Stack Dielectrics", IEEE Electron Device Letters, 19(10), (Oct. 1998), pp. 388-390	

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